

<<电源敏感的计算机系统>>

图书基本信息

书名：<<电源敏感的计算机系统>>

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内容概要

This book contributes the thoroughly refereed post-proceedings of the 4th International Workshop on Power-Aware Computer Systems, PACS 2004, held in Portland, OR, USA in December 2004. The 12 revised full papers presented were carefully reviewed, selected, and revised for inclusion in the book. The papers span a wide spectrum of topics in power-aware systems; they are organized in topical sections on microarchitecture- and circuit-level techniques, power-aware memory and interconnect systems, and frequency- and voltage-scaling techniques.

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书籍目录

Microarchitecture- and Circuit-Level Techniques An Optimized Front-End Physical Register File with
Banking and Writeback Filtering Reducing Delay and Power Consumption of the Wakeup Logic Through
Bit-Sliced Datapath for Energy-Efficient High Performance Low-Overhead Core Swapping for Thermal
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