<< Embedded Software an>>

图书基本信息

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内容概要

This book constitutes the refereed proceedings of the Second International Conference on Embedded Software and Systems, ICESS 2005, held in Xi'an, China, in December 2005. The 63 revised full papers presented together with the abstracts of 3 keynote speeches were thoroughly reviewed and selected from 361 submissions. The papers are organized in topical sections on embedded hardware, embedded software, real-time systems, power aware computing, hardware/software co-design and system-on-chip, testing and verification, reconfigurable computing, agent and distributed computing, wireless communications, mobile computing, pervasive/ubiquitous computing and intelligence, multimedia and human-computer interaction, network protocol, security and fault-tolerance, and abstracts of eight selected workshop papers.

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书籍目录

Keynote Speech Are Lessons Learnt in Mobile Ad Hoc Networks Useful for Wireless Sensor Networks? Compiler-Directed Scratchpad Memory Management Heterogeneous Multi-processor SoC: An Emerging Paradigm of Embedded System Design and Its Challenges Track 1: Embedded Hardware Trace-Based Runtime Instruction Rescheduling for Architecture Extension Bioinformatics on Embedded Systems: A Case Study of Computational Biology Applications on VLIW Architecture The Design Space of CMP vs. SMT for High Performance Embedded Processor Reconfigurable Microarchitecture Based System-Level Dynamic Power Management SoC PlatformTrack 2: Embedded Software A Methodology for Software Synthesis of Embedded Real-Time Systems Based on TPN and LSC Ahead of Time Deployment in ROM of a Java-OS The Research on How to Reduce the Number of EEPROM Writing to Improve Speed of Java Card A Packet Property-Based Task Scheduling Policy for Control Plane OS in NP-Based Applications RBLS: A Role Based Context Storage Scheme for Sensornet CDP: Component Development Platform for Communication Protocols TrieC: A High-Speed IPv6 Lookup with Fast Updates Using Network Processor Separate Compilation for Synchronous Modules Implementation of Hardware and Embedded Software for Stream Gateway Interface Supporting Media Stream Transmissions with Heterogeneous Home Networks Track 3: Real-Time Systems On Using Locking Caches in Embedded Real-Time Systems Trace Acquirement from Real-Time Systems Based on WCET Analysis Elimination of Non-deterministic Delays in a Real-Time Database SystemTrack 4: Power-Aware Computing Track 5: Hardware/Software Co-design and System-On-ChipTrack 6: Testing and Verfication Track 7: Reconfigurable Computing Track 8: Agent and Distributed Computing Track 9: Wireless Communications Track 10: Mobile Computing Track 11: Pervasive/Ubiquitous Computing and Intelligence Track 12: Multimedia and Human-Computer Interaction Track 13: Network Protockl, Security and Fault-Tolerance Track 14: Workshop Seleted PapersKeynote SpeechAuthor Index

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