

<<数字设计>>

图书基本信息

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内容概要

芯片技术的发展使IT界的“摩尔定律”一次次闪耀出智慧的光芒，数字电路的设计技术同样更是不断地获得突破。

使学生能跟上“摩尔定律”，掌握先进的设计技术，正是本书的目的与特色。

本书内容翔实，基本原理描述清晰准确，设计实例丰富全面，适合设计、构建数字电路的各个层次的读者选用。

本书立足于数字设计相对稳定的基本原理，这些原理包括组合逻辑、时序逻辑和状态机等。

作者将这些原理与体现当前先进设计技术的工具和设计技巧有机地结合起来，包括如何使用ABEL和VHDL设计语言；如何进行结构化设计；如何通过可编程逻辑器件来实现最终的设计等。

全书共分十一章，内容涵盖了基本原理，组合与时序逻辑设计的原理、实践和实例，以及当前的发展状况。

本书图文并茂，写作风格简洁、明了，更有富于指导意义的练习题。

本书可作为电子、计算机专业本科及研究生学习数字逻辑设计的入门教材，也可作为工程技术人员的参考书。

内容：1. 导论 2. 数制与编码 3. 数字电路 4. 组合逻辑设计原理 5. 组合逻辑设计实践 6. 组合电路设计实例 7. 时序逻辑设计原理 8. 时序逻辑设计实践 9. 时序电路设计示例 10. 内存、复杂可编程逻辑器件和现场可编程门阵列 11. 其他应用领域

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