

<<从ASIC到SOC>>

图书基本信息

书名：<<从ASIC到SOC>>

13位ISBN编号：9787111175742

10位ISBN编号：7111175743

出版时间：2006-1

出版时间：机械工业

作者：内库加

页数：188

版权说明：本站所提供下载的PDF图书仅提供预览和简介，请支持正版图书。

更多资源请访问：<http://www.tushu007.com>

<<从ASIC到SOC>>

内容概要

Information you can put straight to work! ASICs (Application-Specific Integrated Circuits) and SOC (Systems On a Chip) are among the hottest topics in semiconductor chip design today. If you're a front-end or back-end chip designer looking to learn the latest techniques and methodologies, this book is for you. From ASICs to SOTs: A Practical Approach explains ASIC and SOC design and verification for the real world--by covering the same issues and dilemmas you'll face on the job! An emphasis on techniques and principles over the particulars of design tools gives you a deeper understanding of the material and will make you much more effective in your work, regardless which tools you use. Topics include:

- Methodologies and design flows for front-end and back-end designs
- Modern physical design techniques
- Integration of IPs on SOC designs
- Low-power design techniques and methodologies
- VoIP (Voice over IP) and STB (Set-Top Box) SOC design examples
- Tips and guidelines for front-end and back-end designs

With content centering on practical and current information and techniques, From ASICs to SOC: A Practical Approach is intended for self-study by practical ASIC and SOC engineers. It also makes an excellent reference book for graduate students in electrical engineering.

书籍目录

List of Abbreviations Preface Acknowledgments
1 Introduction 1.1 Introduction 1.2 Voice Over IP SOC 1.3 Intellectual Property 1.4 SOC Design Challenges 1.5 Design Methodology 1.6 Summary 1.7 References
2 Overview of ASICs 2.1 Introduction 2.2 Methodology and Design Flow 2.3 FPGA to ASIC Conversion 2.4 Verification 2.5 Summary 2.6 References
3 SOC Design and Verification 3.1 Introduction 3.2 Design for Integration 3.3 SOC Verification 3.4 Set -Top-Box SOC 3.5 Set-Top-Box SOC Example 3.6 Summary 3.7 References
4 Physical Design 4.1 Introduction 4.2 Overview of Physical Design Flow 4.3 Some Tips and Guidelines for Physical Design 4.4 Modern Physical Design Techniques 4.5 Summary 4.6 References
5 Low-Power Design A Low-Power Design tools B Open Core Protocol(OCP) C Phase-Locked Loops(PLLs) Glosary Index

<<从ASIC到SOC>>

版权说明

本站所提供下载的PDF图书仅提供预览和简介，请支持正版图书。

更多资源请访问:<http://www.tushu007.com>